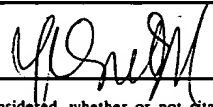
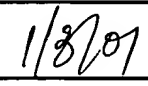


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*Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate	
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	AB	5,023,683	06/11/91	Yamada	357	23.6		
	AC	5,032,882	07/16/91	Okumura et al.	357	23.6		
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Q	AK	5,250,457	10/05/93	Dennison	437	48		
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							Yes	No
B	AL	DE 42 20 497 A1	23.06.92	Germany				
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OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, Etc.)								
C	AR	IBM Corp., <i>Stacked Capacitor DRAM Cell With Vertical Fins (VF-STC)</i> , 33 IBM TECHNICAL DISCLOSURE BULLETIN, No. 2, pp. 245-247 (July 1990).						
	AS	Toru Kaga et al., <i>Crown-Shaped Stacked-Capacitor Cell for 1.5-V Operation 64-Mb DRAMS's</i> , 38 IEEE TRANSACTIONS ON ELECTRON DEVICES, No. 2, pp. 255-260 (February 1991).						
	AT	K. Ueno et al., <i>A Quarter-Micron Planarized Interconnection Technology With Self-Aligned Plug</i> , IEEE, pp. 305-308 (1992).						
EXAMINER					DATE CONSIDERED			
								
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